



B. P. Poddar Institute of Management & Technology
Department of Electronics & Communication Engineering



Academic Year: 2018-19, Semester: 3rd

Laboratory Name: Boylestad Lab Room No.: B 502

Course Name: Analog & Digital Electronic Circuit Laboratory (EC(EE)-391)

2 EE 1st Semester

List of Experiments to be conducted

Sl. No.	Name of Experiment	CO	PO	PSO
1	Study of ripple and regulation characteristics of full wave rectifier without and with capacitor filter	1, 2,3,5	1,2,3,9	1
2	Study of Zener diode as a voltage regulator	1, 2,3,5	1,2,3,9	1
3	Study of timer circuit using NE 555 & configuration for monostable & astable multivibrator	1, 2,3,5	1,2,3,9	1
4	Realization of a V- to - I & I - to - V converter using Op - Amps	1, 2,3,5	1,2,3,9	1
5	Realization of basic gates using universal logic gates	4,5	1,2,3,9	1
6	Construction of simple Decoder & Multiplexer circuits using logic gates	4,5	1,2,3,9	1
7	Realization of RS / JK / D flip flops using logic gates	4,5	1,2,3,9	1
8	Realizations of Synchronous Up/Down counter	4,5	1,2,3,9	1
9	Study of D.A.C & A.D.C	1, 2,3,5	1,2,3,9	1
10	Construction of two stage R-C coupled amplifier & study of its gain & Bandwidth	1, 2,3,5	1,2,3,9	1

11	Construction of adder circuit using Shift register & Full adder	4,5	1,2,3,9	1
12	Design of Combinational circuit for BCD to decimal conversion to drive 7-segment display using Multiplexer	4,5	1,2,3,9	1
13	Any one additional experiment:			
	a) Design of RC phase shift Oscillator	1, 2,3,5	1,2,3,9	1
	b) Design of code converters	4,5	1,2,3,9	1