



B. P. PODDAR INSTITUTE OF MANAGEMENT & TECHNOLOGY

Department of Electronics & Communication Engineering

Academic Year: 2018 - '19, Semester: Even

Laboratory Name: Sir J. C. Bose Lab

Room No.: B 504

Course Name: Communication Engineering & Coding Theory Lab

(CS491)

2 CSE/ IT 2nd Semester

List of Experiments to be conducted



Sl. No.	Name of Experiment	CO	PO	PSO
1	Module - 1: Generation of Amplitude Modulation (Design using transistor or Balanced Modulator Chip. (to view the wave shapes)	1, 3, 4	1	-
2	Module - 2: Generation of FM using VCO chip. (to view the wave shapes)	1, 2, 3	2	-
3	Module - 3: Generation of PAM using IC 4016.	5	1	-
4	Module - 4: Generation of PWM (using IC 555 Timer)	5	1	-
5	Module - 5: Generation of PPM (using IC 555 Timer)	5	1	-
6	One Additional Experiment: a) Study of Voltage Controlled Oscillator (VCO). b) Study of Double - Sideband Suppressed Carrier (DSB-SC).	1	1	-
7	One Micro Project: a) Design of Phase Locked Loop. b) Design of Ring Modulator. c) Design of Audio Mixer. d) Design of Frequency Jammer.	5	1	-