



B. P. PODDAR INSTITUTE OF MANAGEMENT & TECHNOLOGY

DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

LABORATORY NAME: VON NEUMANN LAB

ACADEMIC YEAR: 2018-2019 ODD SEMESTER

List of Experiments

Experiment No.	Name of Experiment	Mapped CO
<i>1</i>	Verification of basic gates and realization of basic gates with universal gates.	CO1
<i>2</i>	Familiarity with IC chips, e.g., a) Multiplexer b) Decoder c) Encoder d) Comparator	CO1
<i>3</i>	Design a Full Adder/ Subtractor composite unit.	CO2
<i>4</i>	Design a BCD adder	CO2
<i>5</i>	Design of a 'Carry Look Ahead' Adder circuit.	CO2
<i>6</i>	Use a multiplexer unit to design a composite ALU.	CO2
<i>7</i>	Use ALU chip for multi bit arithmetic operation.	CO3
<i>8</i>	Implement read write operation using RAM IC.	CO4
<i>9</i>	Cascade two RAM ICs for vertical and horizontal expansion.	CO5
<i>10</i>	Design a adder circuit with a 3* 8 decoder.	CO2
<i>11</i>	Beyond syllabus & innovative experiments	-
<i>12</i>	<i>Micro Project</i>	-