

EC-702

QUIZ 1

1. What are four generations of Integration Circuits?
2. Give the advantages of IC.
3. Mention the advantages of CMOS (any 2).
4. What are the different operating regions for an MOS transistor?
5. Define NM_H .

QUIZ 2

1. Mention the basic process for IC fabrication.
2. State Fick's first law of diffusion.
3. Define 3 types of CMOS fabrication process.
4. Mention the domains of the "Y-Chart".
5. What do you understand by photoresist?

QUIZ 3

1. What is pull down device?
2. What is pull up device?
3. Why NMOS technology is preferred more than PMOS technology?
4. Give the different symbols for transmission gate.
5. Write expression of I_{DSS} for saturation region.

QUIZ 4

1. Define a current mirror?
2. Define switched capacitor filter?
3. Define voltage reference?
4. Define current Sink.
5. Define current Source.

QUIZ 5

1. Define entity in VHDL.
2. Define architecture in VHDL..
3. Mention different architecture models.
4. Mention library packages of in VHDL.
5. Write 2 differences between VHDL and VERILOG.