



B. P. PODDAR INSTITUTE OF MANAGEMENT & TECHNOLOGY

Assignment/ B.TECH/ ECE (NEW)/ SEM 7/ EC702/ 2018

Microelectronics & VLSI Designs EC-702

Full Marks: 25

OUTCOME BASED EDUCATION (OBE)			
Question No.	Knowledge Domain	Allotted Marks	COs
1	Understand	5	1
2	Apply	5	2
3	Apply	5	2
4	Understand	5	4
5	Apply	5	5

- Explain the lambda rule.
 - Draw the layout of CMOS NAND gate
- Explain DCVSL design and **Design** XNOR using DCVSL.
- Design J-K flip flop with CMOS logics.
Or
Draw the circuit of one transistor dynamic RAM cell.
- Explain the circuit of current mirror with proper diagram.
- Explain the term entity and architecture in VHDL and hence **Design** the J-K flip using VHDL