Kunal Das

264/1, Kali Bari 3rd Lane, New Barrackpur, Kolkata -700131

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Educational Qualification:

- **M.Tech in Information Technology from Calcutta university with 75%.**
- **B.Tech in Information Technology(B.Tech) Engg. from Calcutta University with 71%.**
- **B.Sc in Elctronics(HONS) from Calcutta University with 66.875%(I'st Class) .**

Academic Achievements:

- **National Scholarship award from MHRD, Govt. of India for the year of 1996-97.**
- **National Scholarship award from MHRD, Govt. of India for the year of 2001-2002.**

Work Experience:

Type of Job	*Organization*	*Designation*	*Duration*
Academic	• Asansol Engg. College	• Lecturer	• 3rd Aug '04 to 15th June'05.
	• Durgapur Institute of Advanced Technology and Management	• Lecturer	• 16th June'05 to 31st July'06
	• MCKV Institute of Engineering	• Lecturer	• 1 st Aug'06 to 31 st July 2007.
	• B.P.Poddar Institute of Management & Technology.	• Lecturer	• 1st Aug'07 to 30 th June'10.
	• B.P.Poddar Institute of Management & Technology.	• Asst. Prof.	• 1st July '10 to till now.

Research Interests: Quantum dot Cellular Automata – NanoComputing.

Teaching Interest: Design Analysis Algorithm, Computer Architecture, Operating System. Internetworking.

List of Publications:

Journal

- Kunal Das, Debashis De "Characterization, Test and Logic Synthesis of Novel Conservative & Reversible Logic Gates for QCA", Int. Journal of Nanoscience, World Scientific. Vol 9, No.3 pp 201-214, 2010.
- Kunal Das, Das, Debashis De "QCA Defect and Fault Analysis of Diverse Nanostructure for Implementing Logic Gate" in Int. journal of Recent Trends in Engineering, Finland. Publisher ACEEE.Vol.3 No.1 pp1-5. 2010.
- Kunal Das, Debashis De" A Study on Diverse Nanostructure for Implementing Logic Gate Design for QCA", Accepted for publication in Int. Journal of Nanoscience, World Scientific.
- Kunal Das, Debashis De "Characterization,Applicability and Defect Analysis for Tiles Nanostructure of Quantum Dot Cellular Automata", Journal of Molecular simulation (2010) Accepted.

Conference

- Kunal Das, Debashis De" A Study on Diverse Nanostructure for Implementing Logic Gate Design for QCA" published in International Conference ICANN'09, held on 09th Dec'09 to 11th Dec'09 at IIT Guwahati.
- Kunal Das, Debashis De "A Novel Approach of And-Or-Inverter (AOI) Gate Design for QCA" published in IEEE International Conference CODEC'09, held on 14th Dec'09 to 16th Dec'09 at Kolkata.
- Kunal Das, Debashis De "Novel Approach to Design A Testable Conservative Logic Gate for QCA Implementation" published in IEEE Intr. Conf. of IACC'2010,held on 19th Feb.-20th Feb. 2010.pp.82-87.
- Kunal Das, Debashis De "Universal Reversible Logic Gate Design for Molecular QCA" published in 97th Indian Science Congress 2010 at Thiruvananthapuram on 3rd Jan -7thJan 2010.

Seminar Conference Attended / Organized

- International Conference ICANN'09, IIT Guwahati , held on 09th Dec'09 to 11th Dec'09.
- IEEE International Conference CODEC'09, held on 14th Dec'09 to 16th Dec'09.
- IEEE Intr. Conf. of IACC'2010, Delhi Chapter, ,held on 19th Feb.-20th Feb'10.