

Faculty Name: Prof. (Dr.) Bhaskar Som

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Date of Birth: 31/12/1947

Highest Education: PhD

Work Experience:

Industrial	32 years
Teaching	6 years

Current Teaching Interest:

VLSI Circuits and Design, EM Theory and Propagation, Antenna, Communication

List of Publications (International Paper):

1. "Small Signal Analysis of Harmonic Modes in An Avalanche Diode"- Int.I.Electronics,Vol.28, pp 191 -196, 1970.
2. "A Small Signal Analysis of the second Harmonic Modes in a Uniformly Avalanching p-i-n Diode having Unequal Ionization Rates and Drift Velocities for Electrons and Holes"- Int.I.Electronics, vol-30, pp 365-379, 1971
- 3) "Analysis of Space Charge Induced Negative Resistance in Linearly Graded Avalanching Silicon p-n junctions"- Solid State Electronics, vol.15 pp 39 -44, 1972
- 4) "Small Signal Analysis of IMPATT Device having two Avalanche Layers Interspaced by a Drift Layer" Solid State Electronics vol17, pp -1029-1038, 1974.
- 5) "Dependence of Read Diode Characteristics on the Current Multiplication Factor in the Avalanche Zone" -Proc.IEEE, vol.63, pp 1072-1073, 1975.
- 6) "Effect of Finite Current Multiplication Factor in the Avalanche Zone on the High Frequency Noise Properties of Read Diodes" -Proc. IEEE,Aug,1976

List of Publications (Book):

Reviewed the book "Microelectronics Circuit Design" - R.C.Jaeger & T.N.Blalock, Tata-McGrawHill Companies in 2006.