

Bikromaditty Mondal.

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Educational Qualification:

- M.Tech. (Computer Science & Engineering), University of Calcutta, 2002.
- M.Sc. (Computer & Information Science), University of Calcutta, 2000.

Teaching Experience: 10+ years

- December 2000 – August 2003 as Lecturer, Department of Computer Science, Barrackpore Rastraguru Surendranath College, Barrackpore, 24 PGS(N).
- August 2003 – September 2005 as Lecturer, Department of Computer Science, Derozio Memorial College, Rajarhat, Kolkata.
- September 2005 – June 2009 as Lecturer, Department of Computer Science & Engineering, B. P. Poddar Institute of Management & Technology, Kolkata.
- June 2009 – till date as Assistant Professor, Department of Computer Science & Engineering, B. P. Poddar Institute of Management & Technology, Kolkata.

Research Interests:

- Synthesis, Design and Testing of VLSI Circuit, Reversible Logic Circuit.
- Quantum Computing. • Visual Cryptography & Digital Watermarking.

Teaching Interest:

- Algorithm Design & Analysis • Formal Language & Automata Theory • Soft Computing
- Computer Architecture, Microprocessor Design • Computer Graphics and Image Processing

List of Publications:

1. Bikramaditty Mondal, Pradyut Sarkar, Susanta Chakraborty, “Synthesis of Reversible Logic Circuit using Unitary Matrix”, Proc. of 2nd IEEE International Workshop on Reliability Aware System Design and Test (RASDAT), pp. 84-88, January 2011.
2. Pradyut Sarkar, Bikramaditty Mondal, Susanta Chakraborty, “Optimal Universal Test Set for Bridging Faults Detection in Reversible Circuit Using Unitary Matrix”, Proc. of 2nd IEEE International Workshop on Reliability Aware System Design and Test (RASDAT), pp. 37-42, January 2011.
3. Soumyadip Mjumdar, Debayan Mukherjee, Bikromaditty Mondal, “Dynamic Channel Allocation in Cellular Mobile Environment using Improvised RSU Algorithm”, Proc. of RTCT-2009, pp 112-117, March 2009.
4. Rashmita Chatterjee, Priyanka De, Bikromaditty Mondal, “Data Hiding in Multiple Shares using Image Size Invariant Visual Cryptography”, Proc. of RTCT-2009, pp. 53-57, March 2009.
5. Susanta Chakraborty, Bikromaditty Mondal, Bhargab Bhattacharya, “Fault Diagnosis in Reversible Circuits”, Proc. of 11th VLSI Design and Test Symposium (VDAT), pp. 83-90, August, 2007.

Seminar Conference Attended / Organized:

- Presented a paper in the 2nd IEEE International Workshop on Reliability Aware System Design and Test (RASDAT) at IIT Chennai, India, January 6-7, 2011.
- Attended 24th International Conference on VLSI Design as a fellow at IIT Chennai, India, January 2-6, 2011.
- Attended Wipro Mission 10x Certification Workshop in Teaching and Learning at MCKV Institute of Engineering, Liluah, Howrah, India, July 27-28, 2010.
- Attended Wipro Mission 10x Workshop on Innovative Teaching and Learning Methodologies at B. P. Poddar Institute of Management and Technology, Kolkata, India, April 12-17, 2010.
- Attended the International Convention on freedom in Computer Technology organized by the Institute for Open Technology and Applications (IOTA) at Science City, Kolkata, India, December 26-28, 2008.
- Attended Faculty Enablement Programme (FEP) at Infosys Technologies Limited, Bhubaneswar, India, April 15-25, 2008.
- Presented a paper in the 11th IEEE VLSI Design And Test Symposium (VDAT) at Saha Institute of Nuclear Physics, Kolkata, India, August 8-11, 2007.
- Attended a UCG sponsored Training Programme on Operating System at Jadavpur University, India, December 7, 2006
- Attended as a fellow member in the 14th IEEE Asian Test Symposium (ATS), at Hayat Regency, Kolkata, India, December 18-21, 2005.